

## EAST Search History

| Ref # | Hits | Search Query   | DBs  | Default Operator | Plurals | Time Stamp       |
|-------|------|--|--|------------------|---------|------------------|
| L10   | 1    | ((first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) and fractional and (integer adj divider))).clm.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:36 |
| L11   | 2    | ((first adj ((phase adj locked adj loop) or pll) with fractional) and ((second adj ((phase adj locked adj loop) or pll) with integer ))).clm.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:36 |
| L12   | 4645 | 375/376  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:41 |
| L13   | 93   | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and ((mobile adj station) or MS or (base adj station) or CDMA or wlan) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:41 |
| L14   | 12   | L12 and L13  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:41 |
| L15   | 2    | "6594508".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:41 |
| L16   | 5    | transmit\$4 with receiv\$3 and (two adj dipole adj antenna) and pll  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/05/14 18:41 |

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| L17 | 2   | "5838730".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L18 | 290 | (first adj ((phase adj locked adj loop) or pll)) same ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))            | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L19 | 43  | L12 and L18  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L20 | 3   | (first adj ((phase adj locked adj loop) or pll)) same ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and ( wlan) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L21 | 2   | "6181923".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L22 | 2   | "5875186".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L23 | 10  | ("5038115"   "5072195"   "5302919"   "5374904"   "5382922"   "5414390"   "5559473"   "5579184"   "5657359"   "5734301").PN.  | US-PGPUB;<br>USPAT;<br>USOCR   | OR | ON | 2007/05/14 18:41 |

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| L24 | 2  | "4720688".pn.     | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L25 | 12 | peregrine and pll | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L26 | 2  | "6090648".pn.     | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L27 | 2  | "20060085662".pn. | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L28 | 2  | pe3291            | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L29 | 2  | "6,489,818".pn.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L30 | 3  | "7003686".pn.     | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |

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| L31 | 2    | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and ( wlan)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L32 | 190  | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L33 | 1690 | (first adj ((phase adj locked adj loop) or pll)) and (second adj ((phase adj locked adj loop) or pll))  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L34 | 0    | peredrine   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L35 | 4    | (first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and ( wlan)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L36 | 0    | pe3292  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L37 | 0    | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) WITH SERIE and ((mobile adj station) or MS or (base adj station) OR CDMA) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L38 | 0    | ("2005/0062547").URPN.  | USPAT  | OR | ON | 2007/05/14 18:41 |

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| L39 | 1   | "10/396118"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON | 2007/05/14 18:41 |
| L40 | 93  | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and ((mobile adj station) or MS or (base adj station)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L41 | 1   | "10/664850"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L42 | 2   | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and ( wlan)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L43 | 65  | (first adj synthesizer) and (second adj synthesizer) and ((phase adj locked adj loop) or pll)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L44 | 432 | peregrine  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L45 | 5   | transmit\$4 and receiv\$3 and (two adj dipole adj antenna) and pll   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |

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| L46 | 191 | transmit\$4 and receiv\$3 and (two adj dipole adj antenna)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L47 | 392 | (first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L48 | 268 | internal adj antenna and TDMA  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L49 | 753 | (PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) and (fractional with (divider or synthesizer))                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L50 | 163 | L49 and L12  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L51 | 0   | "10664850"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L52 | 1   | WO99/08456   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |

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| L53 | 109   | "08456"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L54 | 2     | "99/08456"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L55 | 5     | "9908456"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L56 | 8     | internal adj antenna same TDMA  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L57 | 2     | internal adj antenna with TDMA  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L58 | 70907 | internal and antenna  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L59 | 7     | ("5107335"   "5423085"  <br>"5432855"   "5436927"   "5633898"<br>  "5727030"   "5768321").PN. | US-PGPUB;<br>USPAT;<br>USOCR   | OR | ON | 2007/05/14 18:41 |

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| L60 | 3163 | internal adj antenna  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L61 | 976  | internal adj antenna and transceiver  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L62 | 135  | internal adj antenna with transceiver   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L63 | 21   | internal adj antenna and transceiver and pll  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L64 | 190  | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L65 | 3    | internal adj antenna with transceiver and pll   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L66 | 5    | internal adj antenna same transceiver and pll   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |



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| L67 | 4   | internal adj antenna and tranceiver                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L68 | 89  | transmit\$4 same receiv\$3 and (two with dipole with antenna) and pll | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L69 | 89  | transmit\$4 with receiv\$3 and (two with dipole with antenna) and pll | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L70 | 168 | transmit\$4 with receiv\$3 and (two adj dipole adj antenna)           | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L71 | 90  | transmit\$4 and receiv\$3 and (two with dipole with antenna) and pll  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L72 | 5   | transmit\$4 same receiv\$3 and (two adj dipole adj antenna) and pll   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L73 | 16  | transmit\$4 with receiv\$3 with (two adj dipole adj antenna)          | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |

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| L74 | 5   | (first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and mobile adj station | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L75 | 527 | ((PLL or (phase adj locked adj loop))) and ((integer with (divider or synthesizer)) with (fractional with (divider or synthesizer)))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L76 | 3   | transmit with receive with (two adj dipole adj antenna)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L77 | 589 | (PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) same (fractional with (divider or synthesizer))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L78 | 527 | (PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) with (fractional with (divider or synthesizer))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |
| L79 | 43  | (second adj (PLL or (phase adj locked adj loop))) and ((integer with (divider or synthesizer)) with (fractional with (divider or synthesizer)))  | US-PGPUB;<br>USPAT;<br>USOCR;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/05/14 18:41 |

 PALM IntranetApplication  
Number

## IDS Flag Clearance for Application 10664850



| Content                               | Mailroom<br>Date | Entry<br>Number | IDS Review                            | Last<br>Modified         | Reviewer   |
|---------------------------------------|------------------|-----------------|---------------------------------------|--------------------------|------------|
| WIDS                                  | 2003-09-22       | 11.7            | Y <input checked="" type="checkbox"/> | 2007-05-14<br>14:54:35.0 | CR #232884 |
| <input type="button" value="Update"/> |                  |                 |                                       |                          |            |

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## Inventor Information for 10/664850

|                      |             |                      |
|----------------------|-------------|----------------------|
| <b>Inventor Name</b> | <b>City</b> | <b>State/Country</b> |
| REUVEN, YOSSI        | HADERA      | ISRAEL               |

|            |          |               |                 |                   |           |
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## Inventor Name Search Result

Your Search was:

Last Name = REUVEN

First Name = YOSSI

| Application#             | Patent#    | Status | Date Filed | Title   | Inventor Name |
|--------------------------|------------|--------|------------|---|---------------|
| <a href="#">10664850</a> | Not Issued | 71     | 09/22/2003 | Method and apparatus to generate signals using two or more phase locked loops | REUVEN, YOSSI |
| <a href="#">60641398</a> | Not Issued | 159    | 01/04/2005 | Wired networking  | REUVEN, YOSSI |
| <a href="#">60659913</a> | Not Issued | 159    | 03/10/2005 | Access network  | REUVEN, YOSSI |
| <a href="#">60682820</a> | Not Issued | 159    | 05/20/2005 | Multi drop DSL  | REUVEN, YOSSI |
| <a href="#">60688711</a> | Not Issued | 159    | 06/09/2005 | DSL over coax   | REUVEN, YOSSI |

Inventor Search Completed: No Records to Display.

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| REUVEN           | YOSSI             | <input type="button" value="Search"/> |

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